

## IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant(s): LEE, Yoon Jik et al.

Application No.:

Group:

Filed:

June 12, 2002

Examiner:

For:

METHOD FOR FABRICATING CELL PLUGS OF SEMICONDUCTOR DEVICE

## LETTER

Assistant Commissioner for Patents Box Patent Application Washington, D.C. 20231

June 12, 2002 0465-0855P-SP

Sir:

Under the provisions of 35 USC 119 and 37 CFR 1.55(a), the applicant hereby claims the right of priority based on the following application(s):

Country

Application No.

Filed

REPUBLIC OF KOREA

P2001-32904

06/12/01

A certified copy of the above-noted application(s) is(are) attached hereto.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. 1.16 or under 37 C.F.R. 1.17; particularly, extension of time fees.

Respectfully submitted,

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